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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Jun KOYAMA et al.

Serial No. 09/902,622

Filed: July 12, 2001

For: DRIVE CIRCUIT OF ACTIVE MATRIX TYPE

DISPLAY DEVICE HAVING BUFFER WITH

PARALLEL CONNECTED ELEMENTAL CIRCUITS

AND MANUFACTURING METHOD THEREFOR



) Art Unit: 2672

) Examiner: Unassigned

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on November 2, 2001.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents and Trademarks
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference listed on the attached Form PTO-1449 be made of record in the above-identified application. A copy of the reference is submitted herewith in accordance with 37 C.F.R. 1.98(a).

Applicants respectfully submit the full English translation of Japanese Utility Model Publication 62-19855 for consideration, which was previously submitted without translation in an Information Disclosure Statement filed September 26, 2001

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380 (740756-2339).

Respectfully submitted,

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